Announcing the Final Examination of Adithi Pandrahal Krishnaprasad Sharada for the degree of Master of Science

Time & Location: June 21, 2018 at 1:00 PM in Research 1 101
Title: High quality gate dielectric/MoS2 interfaces probed by the conductance method

Two-dimensional materials provide a versatile platform for various electronic and optoelectronic devices, due to their uniform thickness and pristine surfaces. We probe the superior quality of 2D/2D and 2D/3D interfaces by fabricating molybdenum disulfide (MoS2)-based field effect transistors having hexagonal boron nitride (h-BN) and Al2O3 as the top gate dielectrics. An extremely low trap density of ~7x10^10 states/cm^2-eV is extracted at the 2D/2D interfaces with h-BN as the top gate dielectric on the MoS2 channel. 2D/3D interfaces with Al2O3 as the top gate dielectric and SiOx as the nucleation layer exhibit trap densities between 7x10^10 and 10^11 states/cm^2-eV, which is lower than previously reported 2D-channel/high-k-dielectric interface trap densities. The comparable values of trap time constants for both interfaces imply that similar types of defects contribute to the interface traps. This work establishes the case for van der Waals systems where the superior quality of 2D/2D and 2D/high-k dielectric interfaces can produce high performance electronic and optoelectronic devices.

Major: Electrical Engineering

Educational Career:
Bachelor's of Electronics and Communications Engineering, BS, 2015, Visvesvaraya Technological University

Committee in Charge:
Tania Roy, Chair, Nanoscience Technology Center and Electrical and Computer Engineering
Reza Abdolvand, Electrical and Computer Engineering
Jiann-Shiun Yuan, Electrical and Computer Engineering

Approved for distribution by Tania Roy, Committee Chair, on June 6, 2018.

The public is welcome to attend.