

2017 COMPUTER ENGINEERING: Digital VLSI Circuits Track

DEGREE REQUIREMENT CHECKSHEET

COLLEGE OF ENGINEERING & COMPUTER SCIENCE

UNIVERSITY OF CENTRAL FLORIDA

GENERAL EDUCATION PROGRAM				LOWER AND JUNIOR LEVEL REQUIRED COURSES			SH	Grd	Trans Equiv
* Indicates "C-" minimum required by the Gordon Rule				EGS 1006C Intro to Engr Prof		1	#		
** Indicates minimum "C" or better grade				EGN 1007C Engr Concepts and Methods		1	#		
COMMUNICATION (9 SEM HRS)	SH	Grd	Trans Equiv	STA 3032 Probability & Statistics for Engrs		GEP			
ENC 1101	3	*		EEL 3926L Junior Design		1			
ENC 1102	3	*		EGN 3211 Engineering Analysis & Comp.		3	**		
				EEL 3004C Electrical Networks		3	**		
SPC 1603	3			EEL 3123C Networks and Systems		3	**		
CULTURAL & HISTORICAL (9 SEM HRS)				EEE 3307C Electronics I		4			
Select 2: AMH 2010, AMH 2020, EUH 2000, EUH 2001, HUM 2211, HUM 2230, WOH 2012, WOH 2022	6	*		EEE 3342C Digital Systems		3	**		
Approved Cultural Foundations course:	3			EEL 3801C Computer Organization		4	**		
SOCIAL FOUNDATION - (6 SEM HRS)				COT 3100C Introduction to Discrete Structures		3	**		
ANT 2000/ PSY 2012/ SYG 2000	3			COP 3330 Object Oriented Programming		3	**		
ECO 2013 <u>or</u> ECO 2023	3			COP 3502C Computer Science I		3	**		
SCIENCE - 6 SH				COP 3503C Computer Science II		3	**		
GEO 1200 <u>or</u> GEO 2370 (either GEO is preferred) <u>or</u> BSC 1050C <u>or</u> BSC 1005C <u>or</u> GLY 1030	3			SENIOR LEVEL REQUIRED COURSES					
PHY 2048C Physics I for Engrs	4			EEL 4742C Embedded Systems		3			
MATHEMATICAL - 6 SH				EEL 4768 Computer Architecture		3			
MAC 2311 Calculus I	4	**		EEL 4781 Computer Communication Networks		3			
STA 3032 Prob & Stats for Engrs	3			RECOMMENDED SENIOR LEVEL ELECTIVE COURSES					
GPA Gen Ed Prog =	38			(CHOOSE MINIMUM 2 FROM LIST)					
ENGINEERING CORE**				EEE 4346C Hardware Security and Trusted Circuit Design		3			
MAC 2311 Calculus I	GEP	**		EEE 4314 Device Electronics for Integrated Circuits		3			
MAC 2312 Calculus II	4	**		EEL 4783 HDL in Digital Systems Design		3			
MAC 2313 Calculus III	4	**		EEL 5704 Computer Aided Logic Design		3			
MAP 2302 Differential Equations	3	**		EEE 5353 Semiconductor Devices Modeling and Simlation		3			
CHS 1440 Chem for Engrs (or CHM 2045C)	4	**		EEE 5356C Fabrication of Solid State Devices		3			
PHY 2048C Physics I for Engrs	GEP	**		EEE 5378 CMOS Analog and Digital Circuit Design		3			
PHY 2049C Physics II for Engrs	4	**		EEE 5390C Full Custom VLSI Design		3			
SUBTOTAL SEM HRS	19			REQUIRED					
				Technical Electives (see list of EEE/EEL/CDA/CAP/COP/COT/C)		15	~		
				EEL 4914 Senior Design I		3			
				EEL 4915L Senior Design II		3			
				SUBTOTAL SEM HRS		71			
				GPA Engr Option =					
				(2.250 minimum)					
** A Grade of C (2.00) or higher required				ADVISOR COMMENTS:					
# Transfer students please see your faculty advisor before registering for these classes.				~BS-MS students should choose (3 SH) 5000 level courses as electives.					